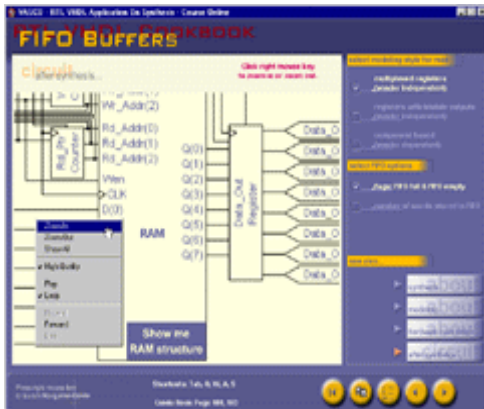


RTL VASCO VHDL Package

VASCO (TM) - RTL VHDL Application on Synthesis - Course Online represents the next generation in distance learning for engineers seeking to further develop their VHDL design skills. It is intended for designers who are already familiar with VHDL and concentrates on developing specific application areas, such as synthesis issues and coding style.



COOK BOOK

21+

sections divided on four groups:
 Combinational, Sequential,
 Level-sensitive and Buses

350+

fully synthesizable models,
 ready-to-use and compliant with
 the most popular synthesis tools:

- FPGA Express from Synopsys,
- Leonardo Spectrum (Exemplar), and
- Synplify (Synplicity)

INTERACTIVE TUTORIAL

70+

interactive pages with simulation
 of the VHDL code, comments about
 synthesis, well described methodology,
 the same examples in various forms

100+

synthesizable VHDL examples,
 immediately ready-to-use, simple
 copy&paste to your favourite
 simulation/synthesis tool



REFERENCE BOOK

38+

RTL VHDL keywords with detailed
 description and up to 6 comments
 and examples

70+

VHDL examples, compliant with
 RTL VHDL Standard 1076.6
 from IEEE

