VASCO
RTL HDL Solutions

for engineers by engineers...

M A B E X
m u l t i m e d i a

Ltd. Company
Ul. Morelowa 57/17
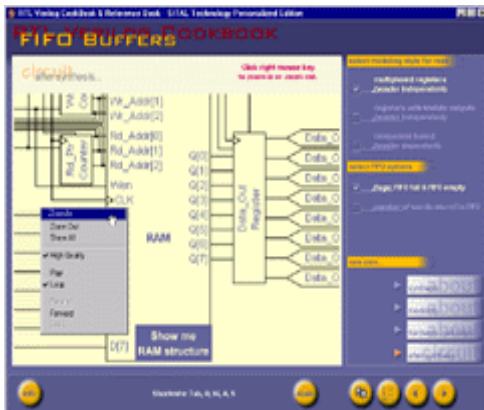65-434 Zielona Góra
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RTL VASCO Verilog Package

VASCO (TM) - RTL Verilog Application on Synthesis - Course Online represents the next generation in distance learning for engineers seeking to further develop their Verilog design skills. It is intended for designers who are already familiar with Verilog and concentrates on developing specific application areas, such as synthesis issues and coding style.

COOK BOOK

21+ sections divided on four groups: Combinational, Sequential, Level-sensitive and Buses

420+ fully synthesizable models, ready-to-use and compliant with the most popular synthesis tools:

- FPGA Express from Synopsys,
- Leonardo Spectrum (Exemplar), and
- Synplify (Synplicity)

REFERENCE BOOK

28+ RTL Verilog keywords with detailed description and up to 9 comments and examples

90+ Verilog examples synthesisable with the top quality synthesis tools